AMD
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Preventing Guests from Spinning Around

How to Deal with Lock-Holder Preemption

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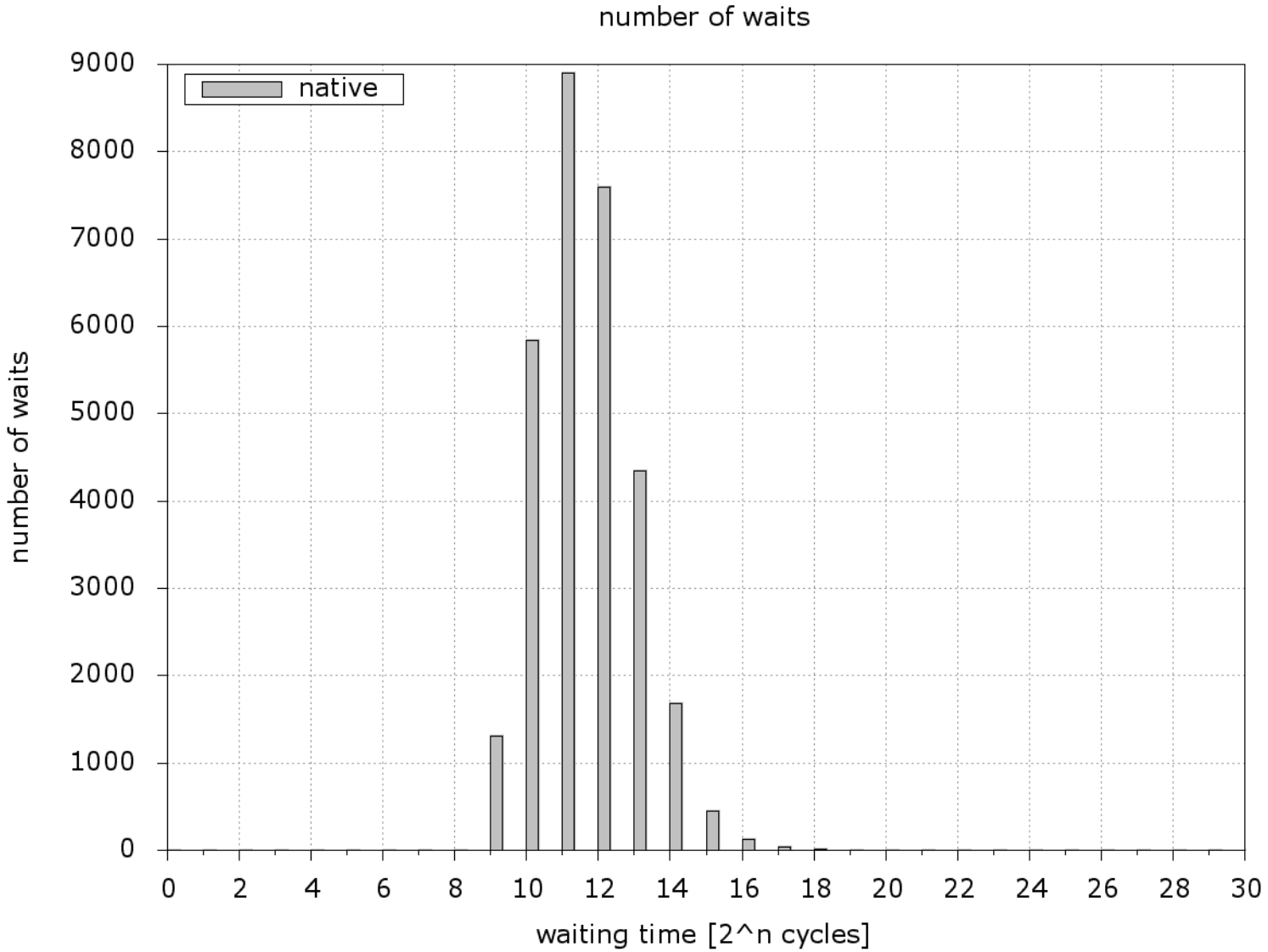
Spinlock Basics

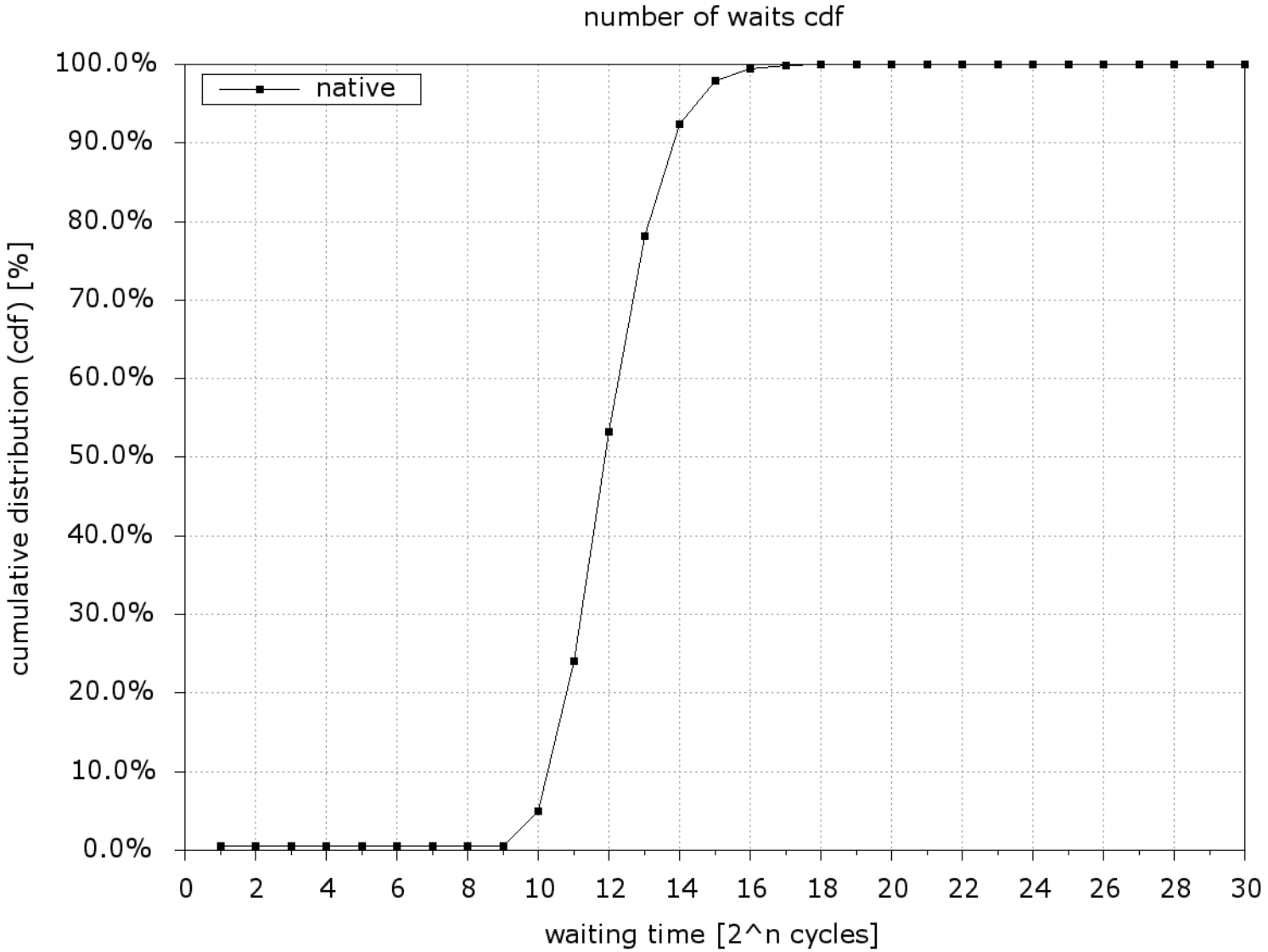
Spinlocks wait actively as opposed to sleeping locks

Used for short critical sections

Spinlock Wait Times – Kernbench



Spinlock Wait Times – Kernbench



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Spinlocks and Virtualization

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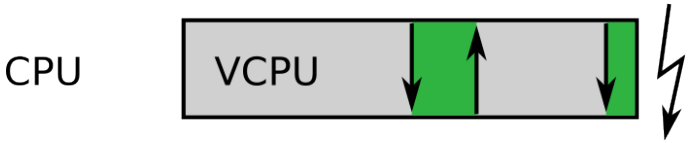
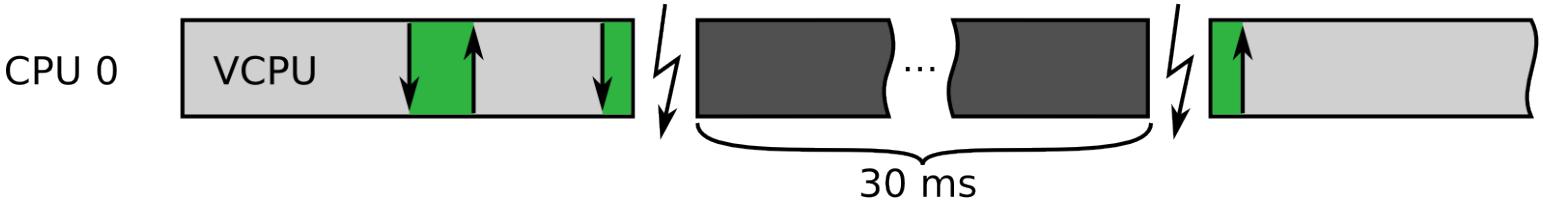
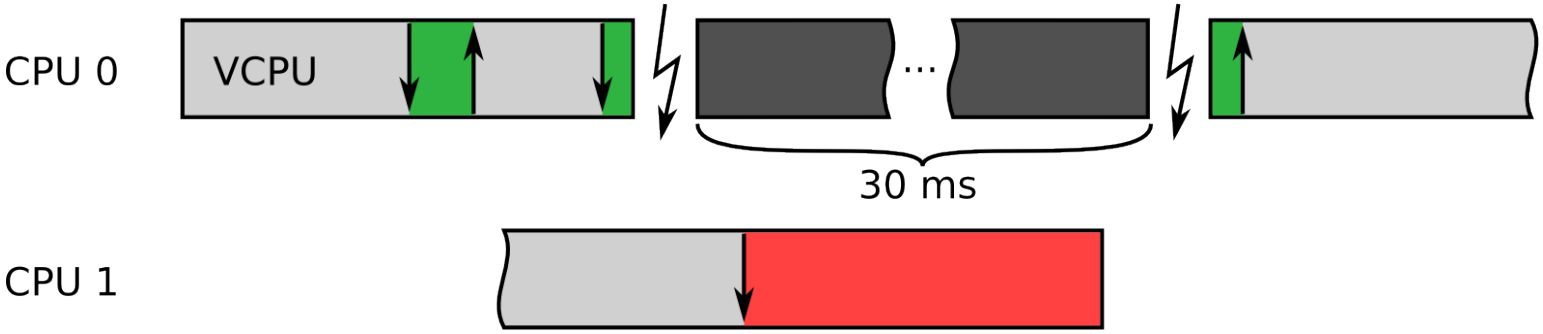


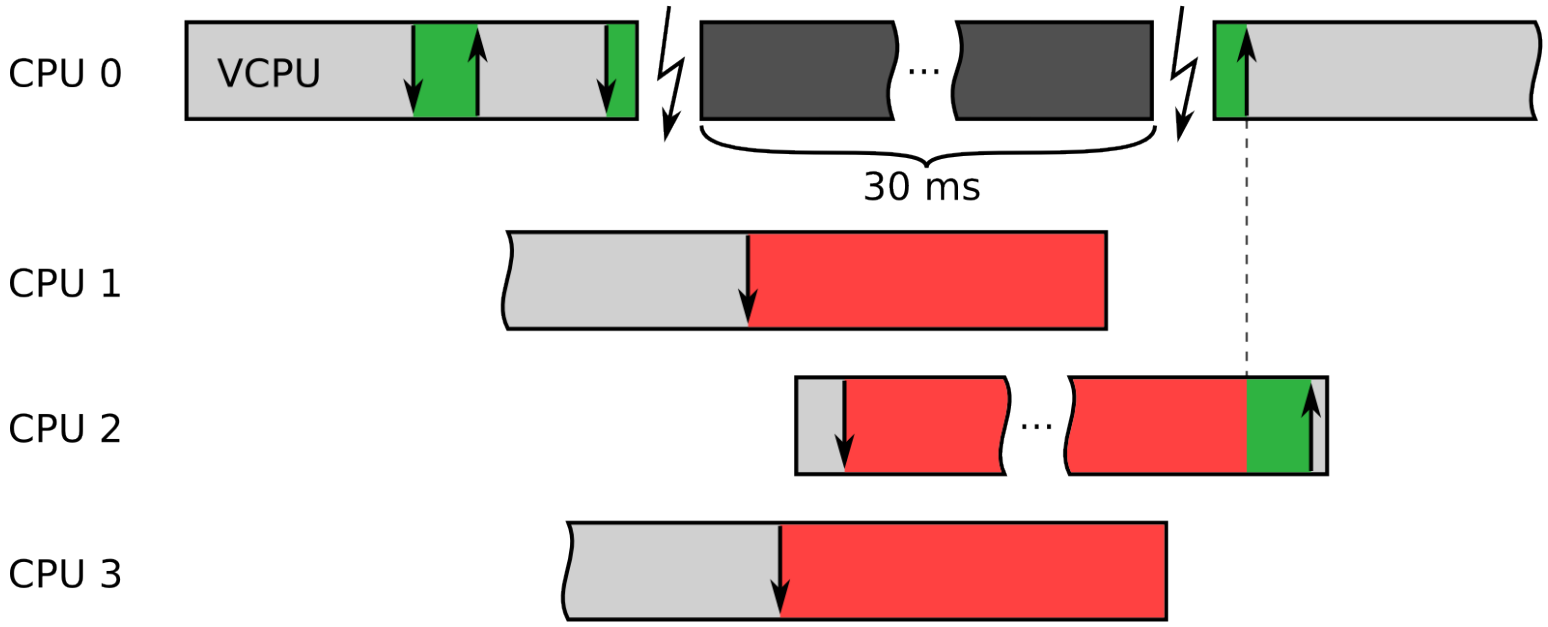
Spinlocks and Virtualization



Spinlocks and Virtualization



Spinlocks and Virtualization



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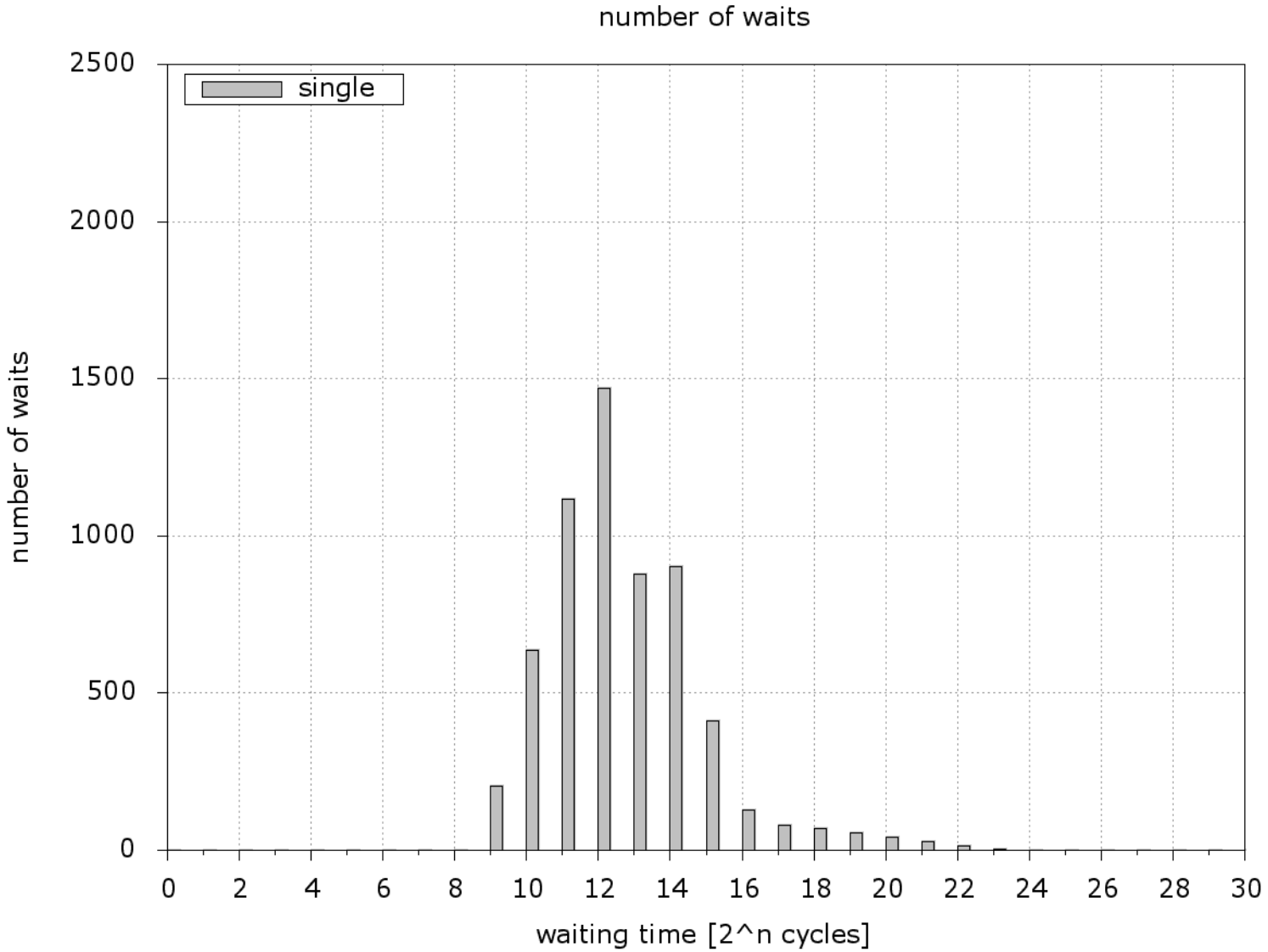
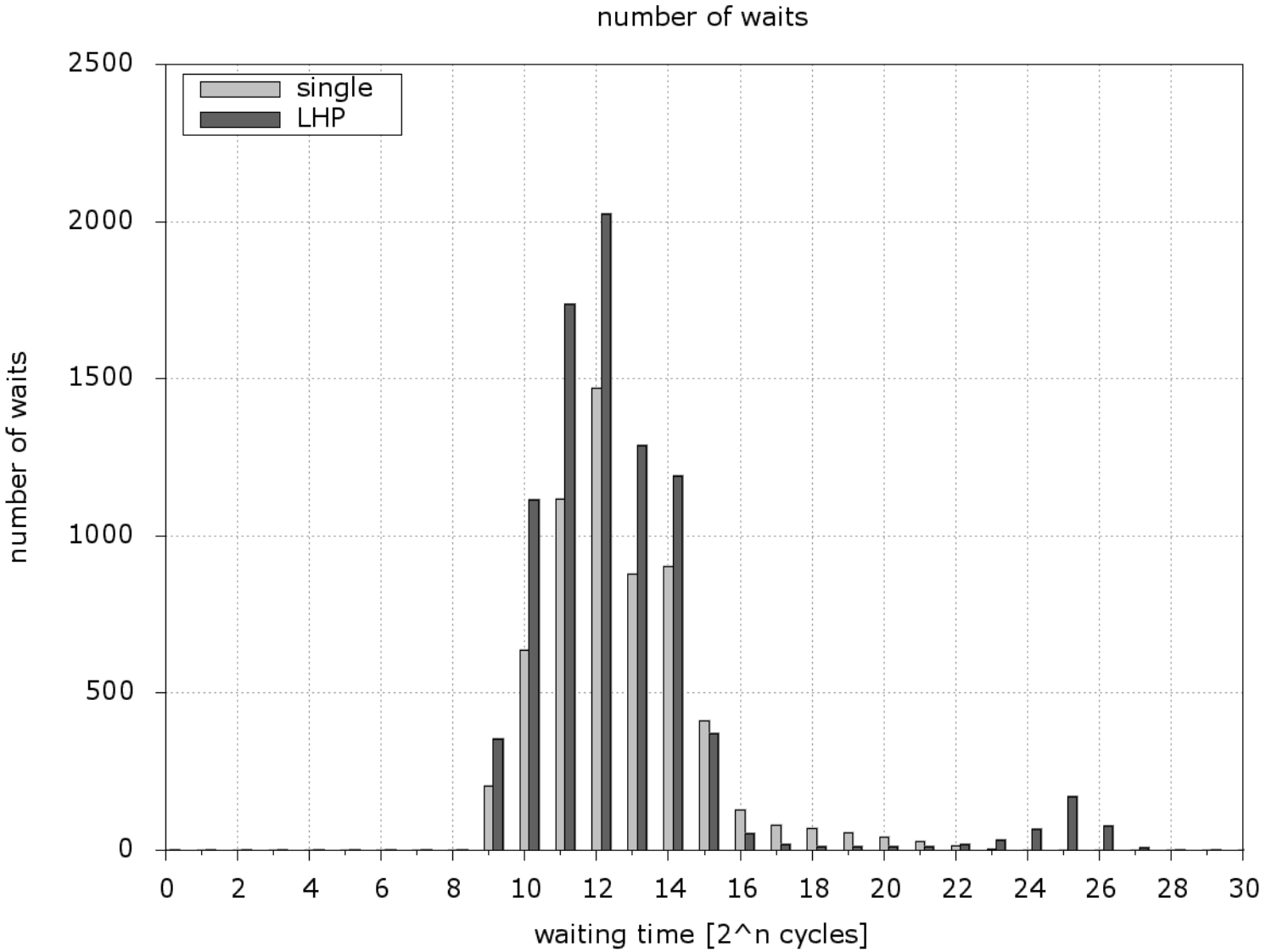
Is lock-holder preemption problematic?

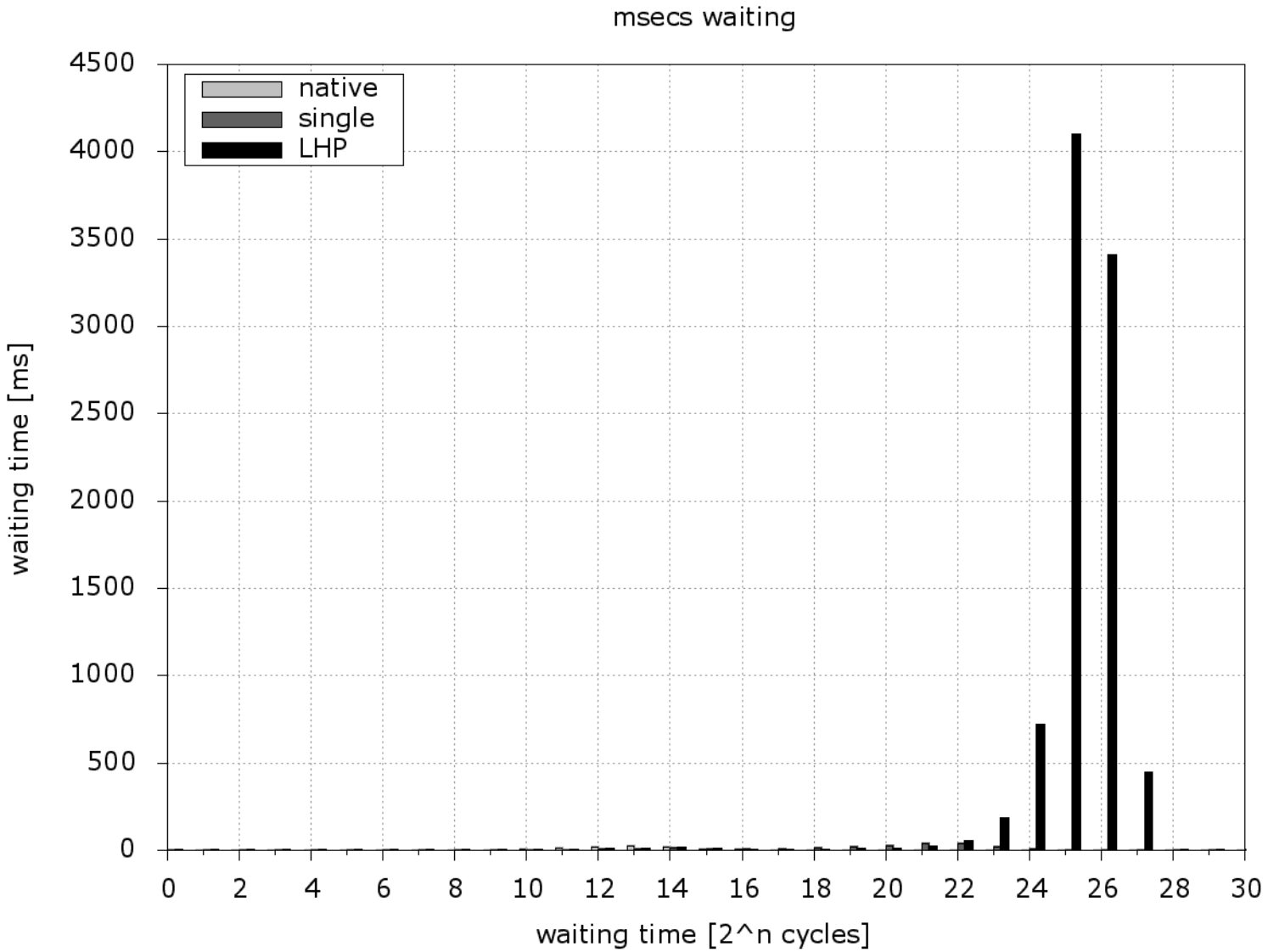
Kernbench in a Guest



Kernbench vs. 'while(true)'



Time, not Times



And in Numbers?

	guest time [s]	time spent spinning [s]	[%]
single kernbench	109.0	0.2	0.2%
kernbench vs while(1)	117.3	9.0	7.6%
difference	7.6%		

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What can we do about it?

Dealing with lock-holder preemption

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LHP avoidance

- No spinlock held in userspace
- Idea: Avoid preempting guest in kernel space
- Postpone guest switch to kernel exit
- Problem: extraordinary long critical sections, e.g. Apache using sendfile()

Helping locks

- Instead of busy waiting, switch to preempted lock-holder
- Problem: finding the preempted lock-holder

Helping locks: Ingredients

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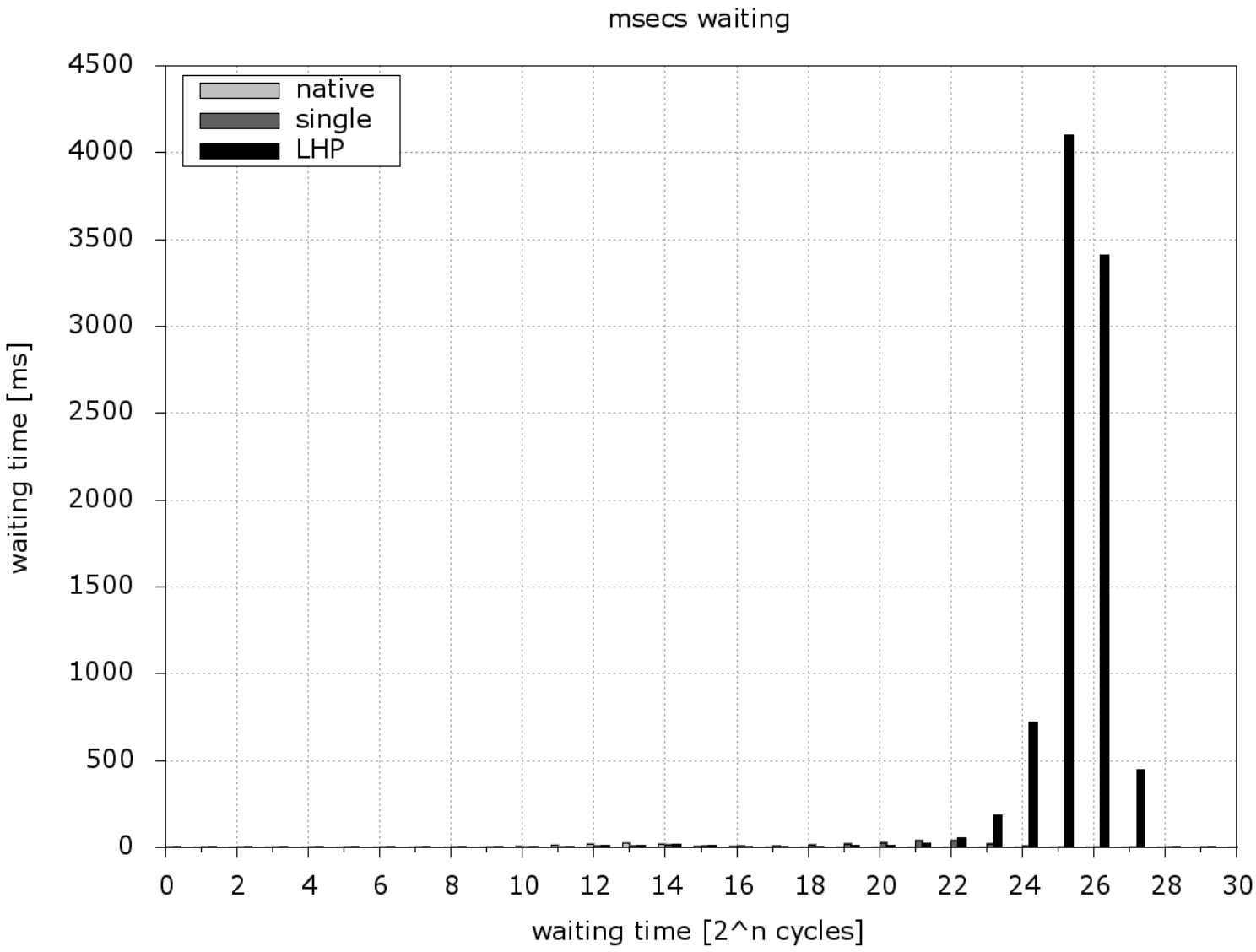
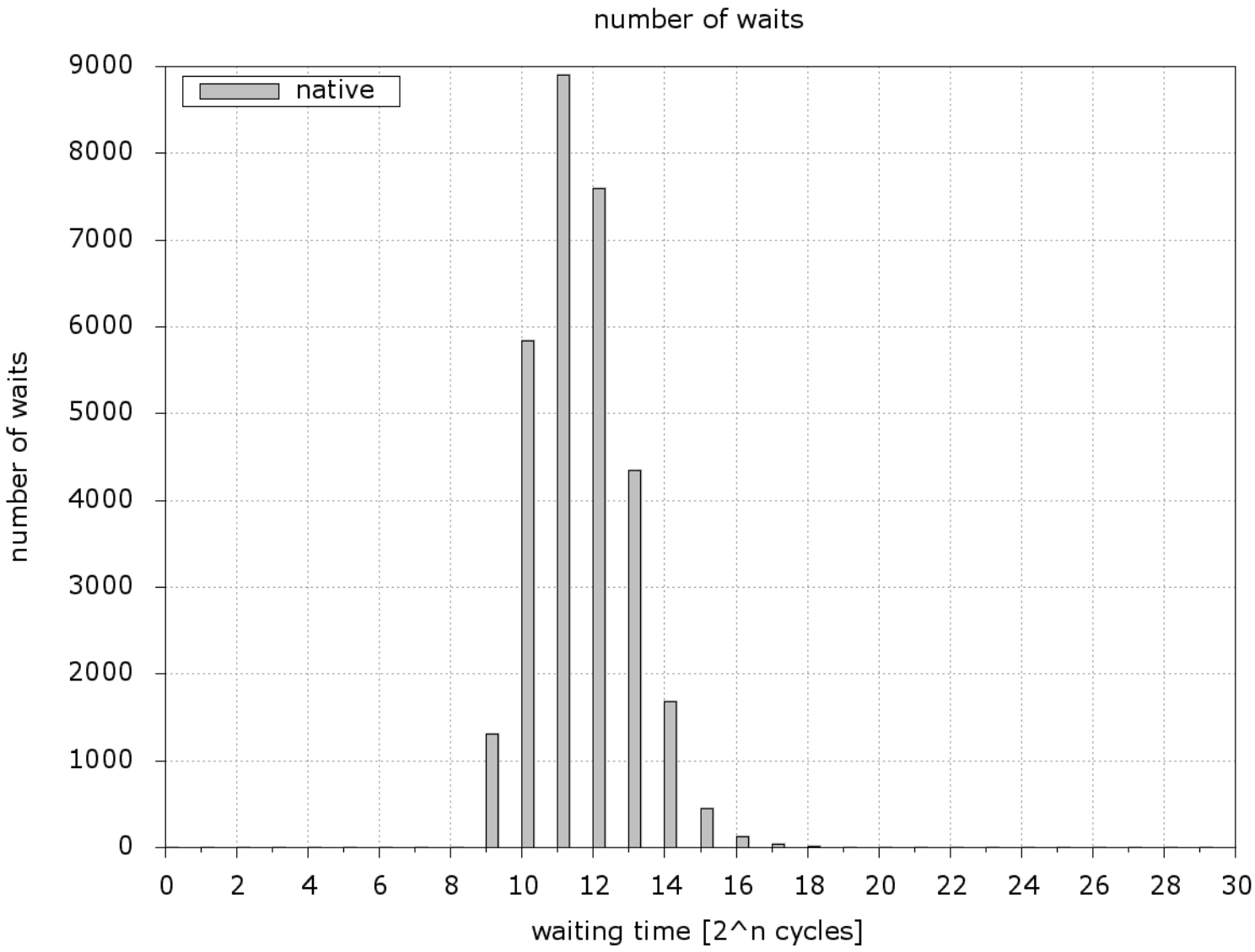
1) Guest kernel: new 'yield' hypercall when waiting unusually long
   - Modify spinlock loop
2) Reasonable threshold for 'unusually long'
   - Histograms help
3) Selecting which VCPU to switch to

Threshold: Upper boundary



Threshold: Lower boundary



Scheduling Strategy

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Good choices:

- VCPUs of the same VM to make progress locally
- (Potential) preempted lock-holders
- Cache-„near“ VCPUs

Neither/nor:

- VCPUs in user space

Bad choices:

- VCPUs which yielded recently

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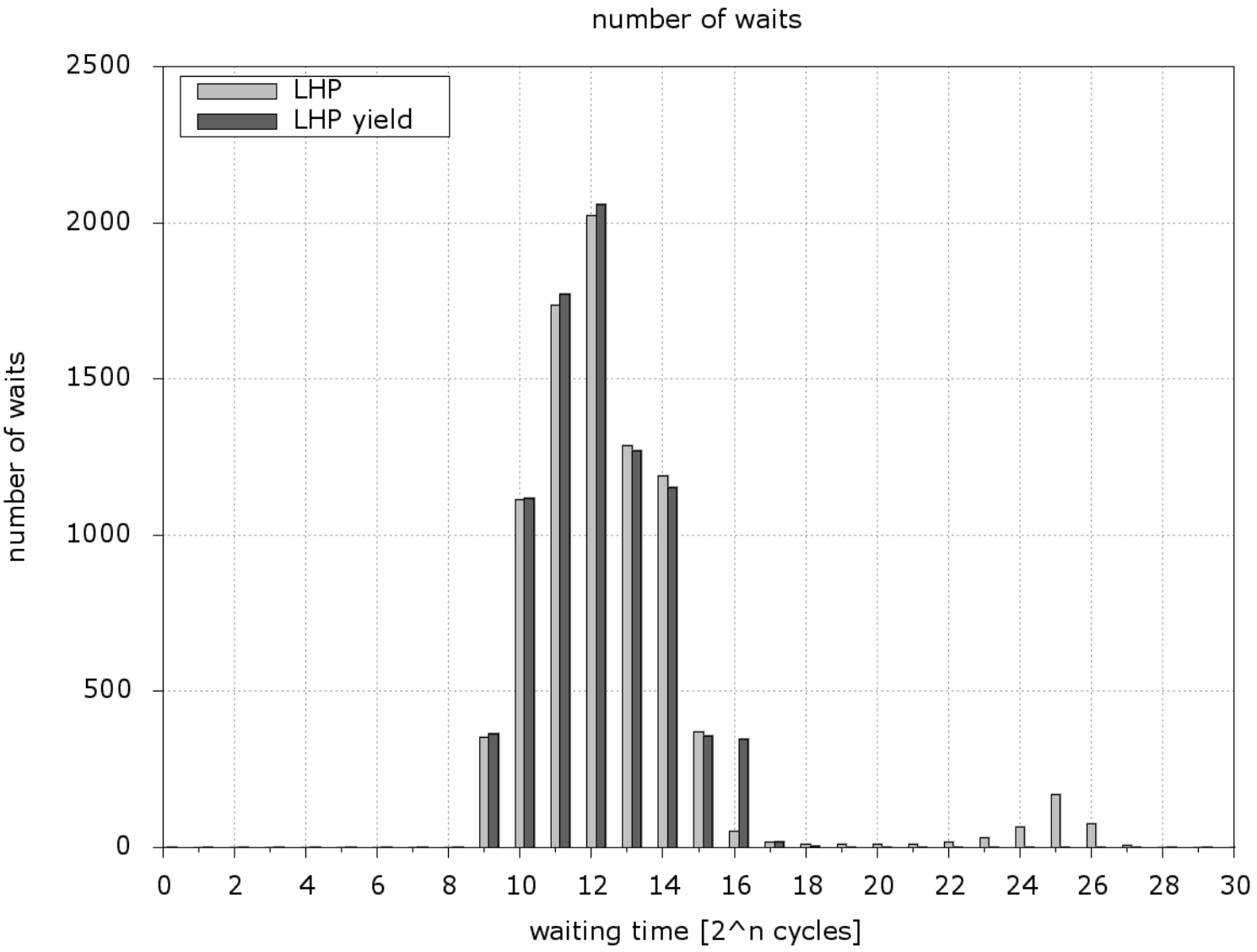
What about performance?

Histogram with 'yield' hypercall



Performance

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	wall clock [s]	guest time [s]	time spent spinning [s]	[%]
LHP	34.8	117.3	9.0	7.6%
yield	33.5	108.4	0.0	0.0%
difference	-3.9%	-7.6%		-7.6%

Efficiency

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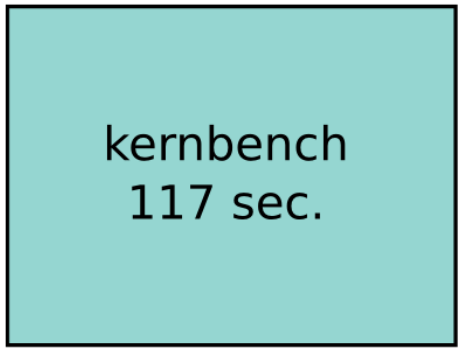
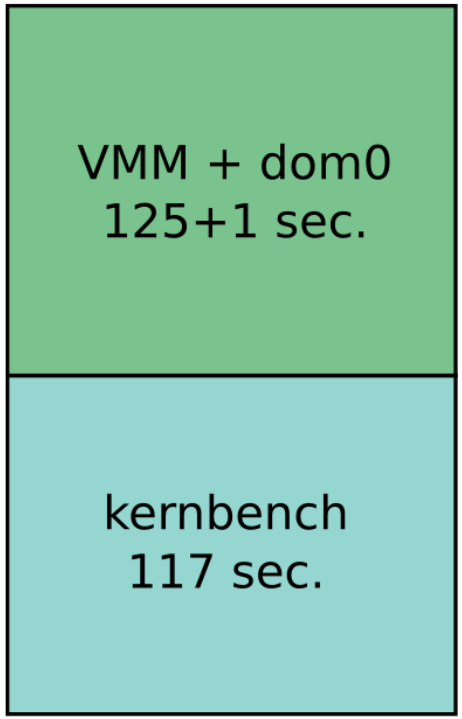
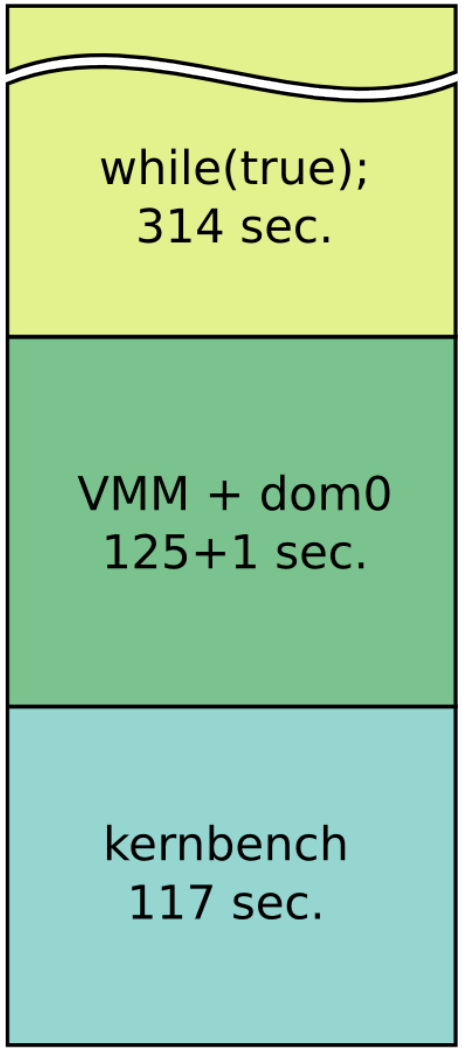


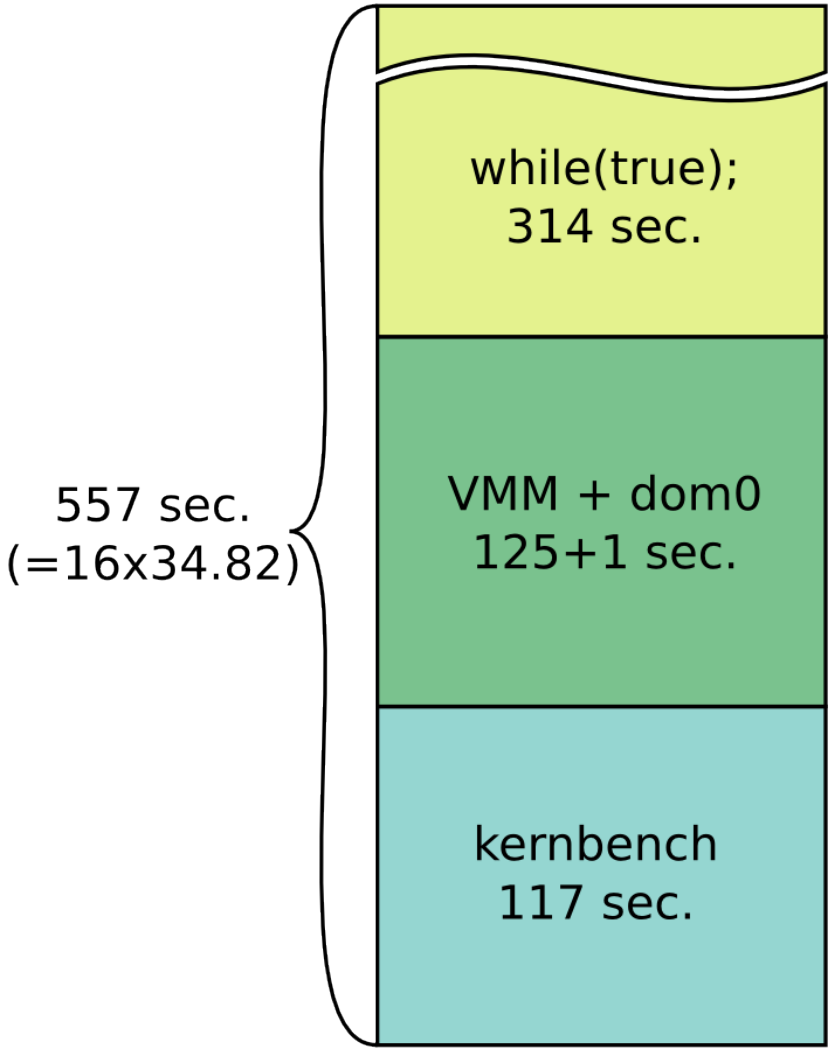
Efficiency



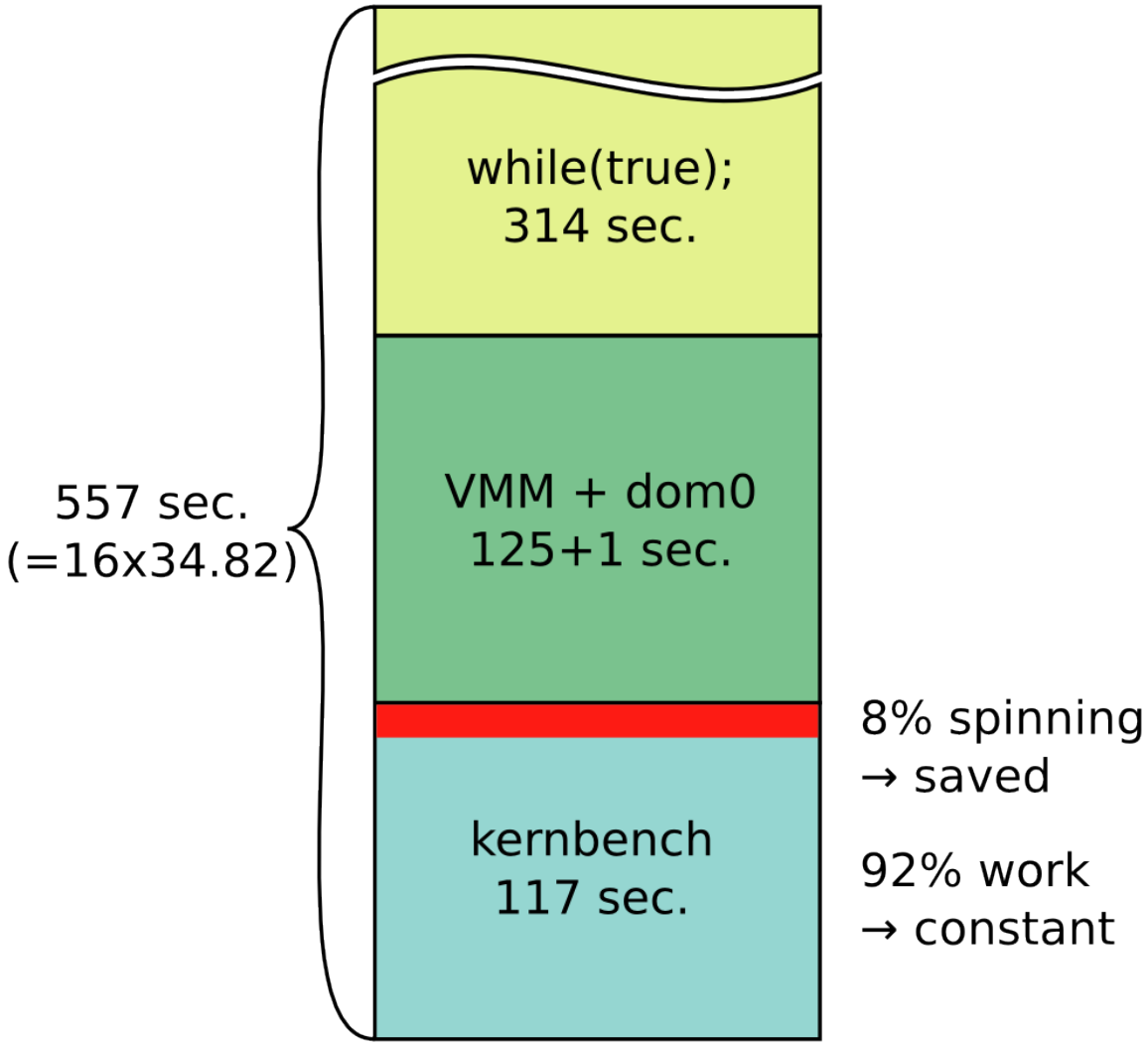
Efficiency



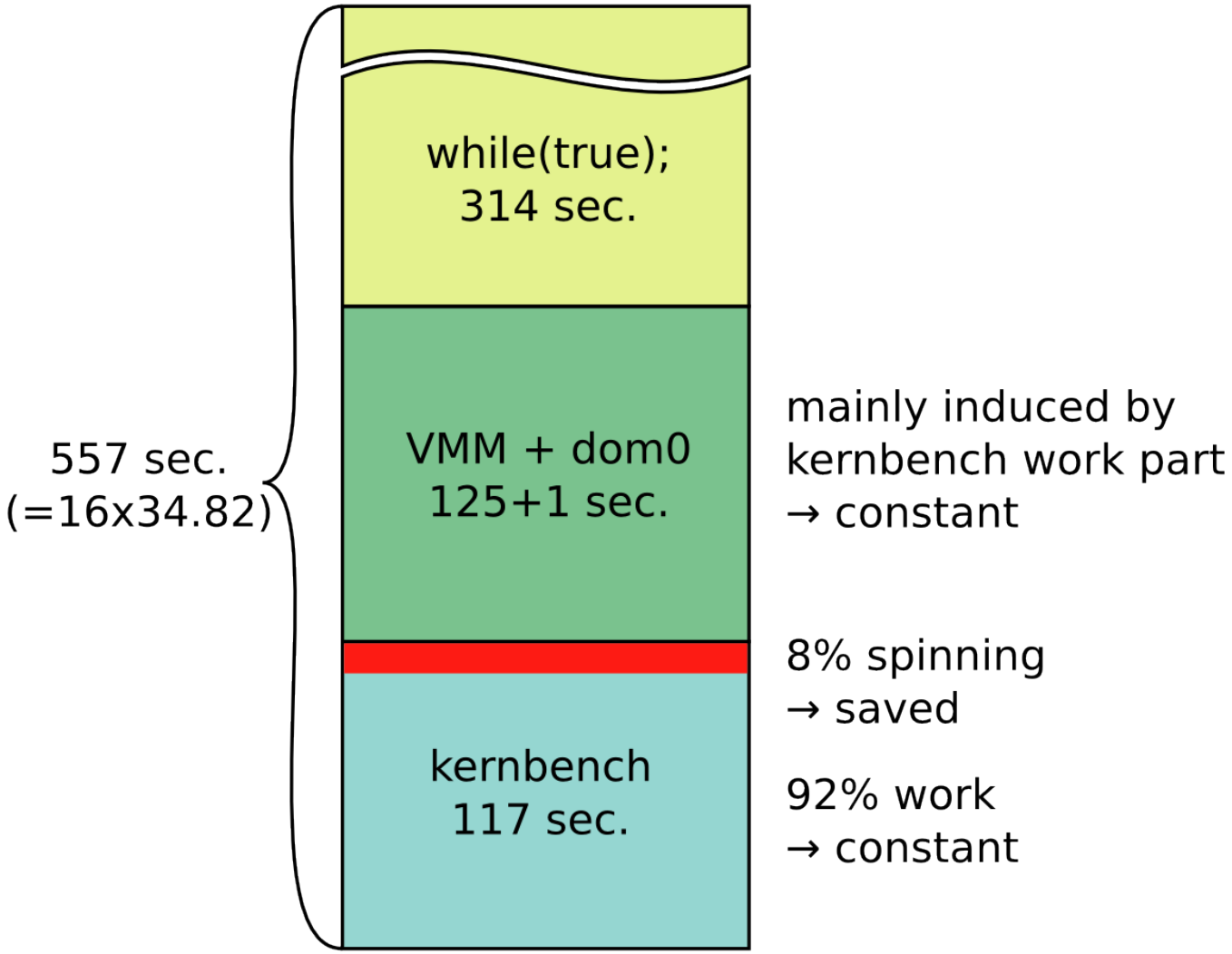
Efficiency


while(true);
314 sec.
VMM + dom0
125+1 sec.
kernbench
117 sec.
557 sec.
(=16x34.82)

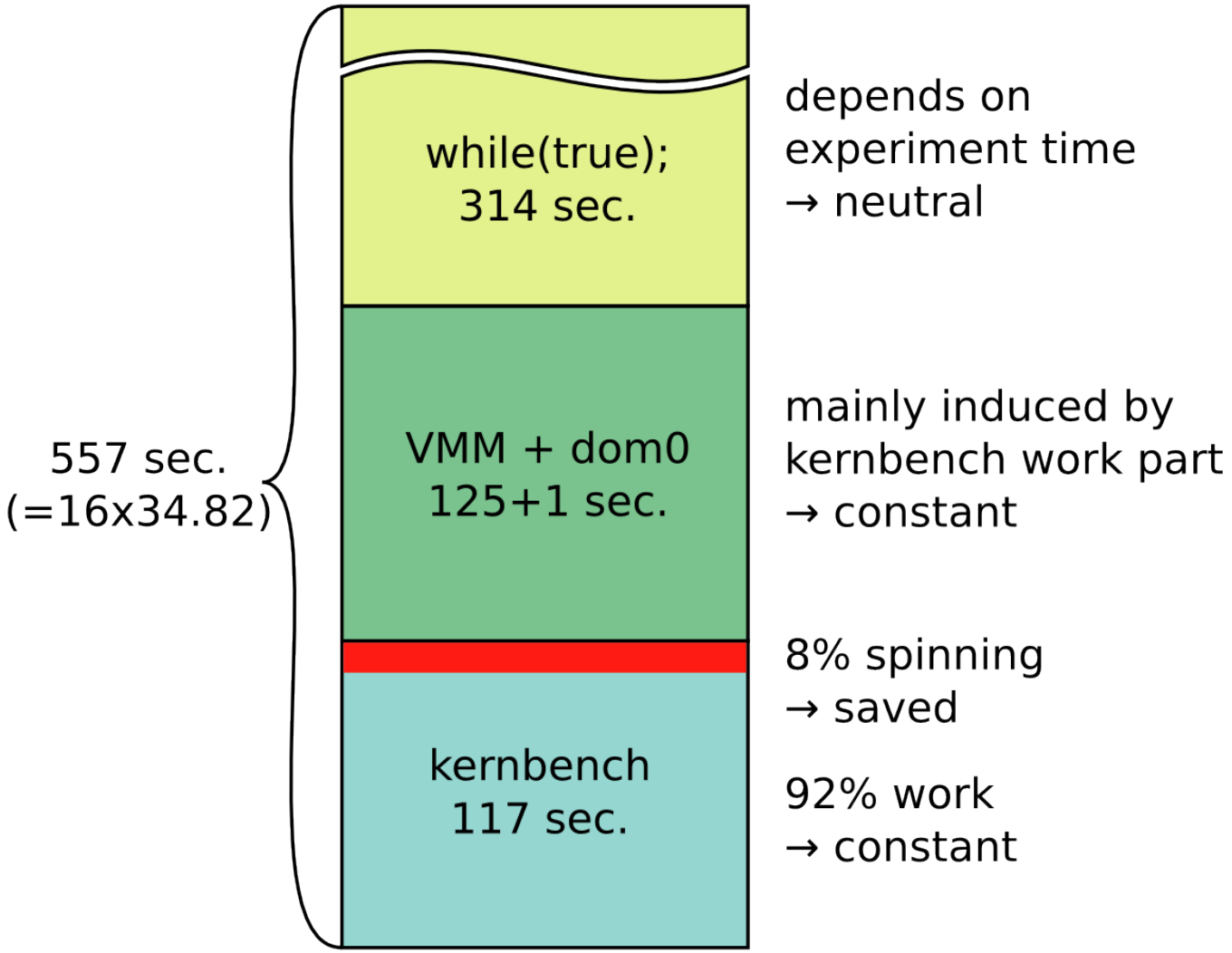
Efficiency


while(true);
314 sec.
VMM + dom0
125+1 sec.
kernbench
117 sec.
557 sec.
(=16x34.82)
8% spinning
→ saved
92% work
→ constant

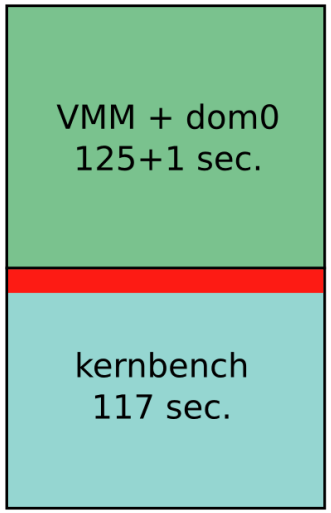
Efficiency


while(true);
314 sec.
VMM + dom0
125+1 sec.
kernbench
117 sec.
557 sec.
(=16x34.82)
mainly induced by
kernbench work part
→ constant
8% spinning
→ saved
92% work
→ constant

Efficiency


while(true);
314 sec.
VMM + dom0
125+1 sec.
kernbench
117 sec.
557 sec.
(=16x34.82)
depends on
experiment time
→ neutral
mainly induced by
kernbench work part
→ constant
8% spinning
→ saved
92% work
→ constant

Efficiency



mainly induced by
kernbench work part
→ constant

8% spinning
→ saved

$$\frac{117\,sec}{117\,sec+126\,sec}\times 7.6\% \;=\; 3.7\%$$

➔ Real result of 3.9% is reasonable

➔ Highly efficient

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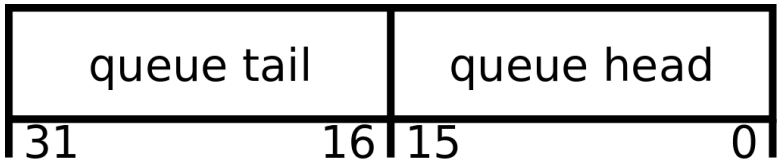
FIFO ticket spinlocks

FIFO ticket spinlocks

Next ticket in dispenser: queue tail

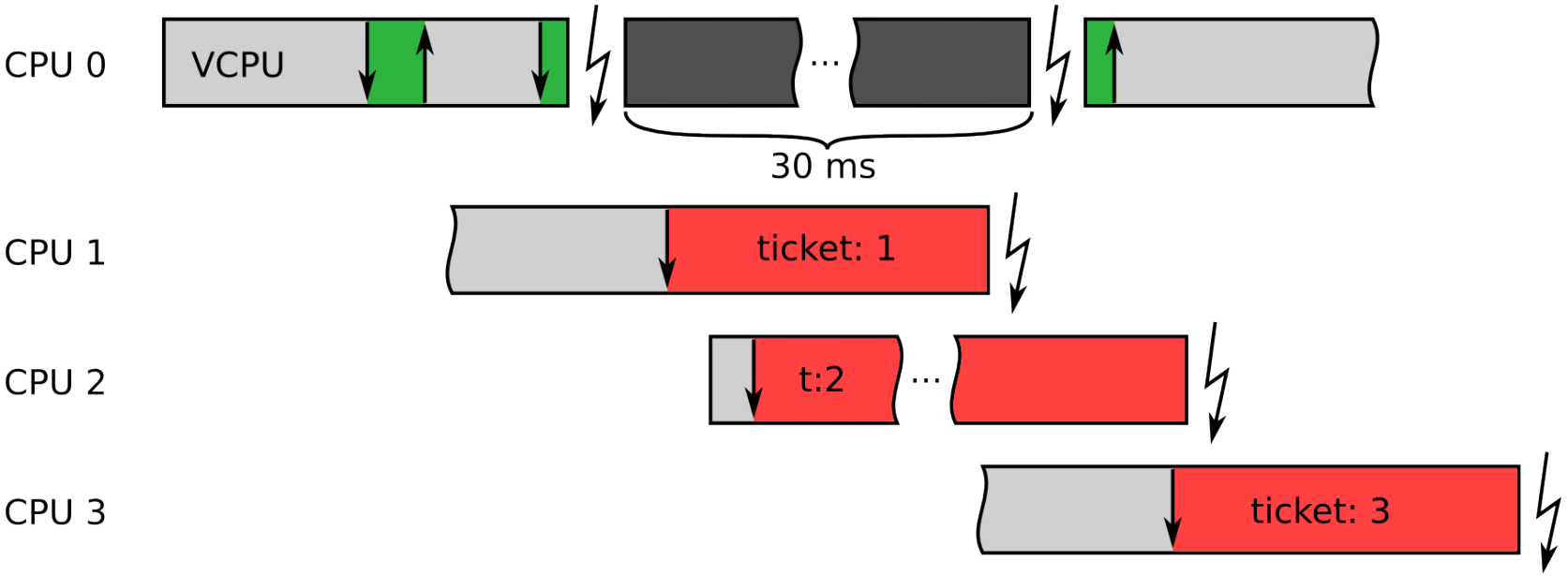
„Now serving“ display at counter: queue head



Lock: atomic(ticket = tail++); while (head != ticket);

Unlock: atomic(head++);

FIFO ticket spinlocks



Ticket locks and virtualization

	wall clock [s]	guest time [s]	time spent spinning [s]	[%]
LHP	2825.1	22434.2	22270.4	99.3%

Ticket locks and virtualization

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	wall clock [s]	guest time [s]	time spent spinning [s]	[%]
LHP	2825.1	22434.2	22270.4	99.3%
yield	34.1	123.6	6.6	5.4%

Conclusion

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Lock-holder preemption quite serious:
7.6% guest time wasted

Helping locks:
3.9% system performance improvement!
(Amdahl's law explains why)

New ticket spinlocks:
30 secs kernbench takes 45 minutes

Helping locks help here, too

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