Characterization & Analysis of a Server Consolidation Benchmark on Xen

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Background

• Virtualization and consolidation are a growing trend in datacenters
  – > 40% of servers expected to run a consolidated workload by 2010

• Problem is there is no analysis methodology or performance studies in place for understanding consolidated workloads
Motivation

- Performance characterization is useful for
  - Providing feedback to IT administrators
    - Deployment with fair share of resources to end-users is challenging with virtualization
  - Providing feedback to Platform Architects
    - To project future platform performance
      - How do apps scale for future platforms
    - To optimize future architectures for consolidation
      - Architectural Effects on consolidation
        - Cache and other resource sharing effects
        - Virtualization overheads effects
  - Providing feedback to VMM developers
    - How are the platforms resources (cores/IO devices scheduled)
    - Scheduling heuristics may be suboptimal without an execution profile
A Consolidation Benchmark

- vConsolidate (vCon) is one of the proposed benchmarks for virtualization consolidation
  - Developed by Intel
  - VMM agnostic
- VMmark is another consolidation benchmark developed by VMware
- vSPEC is a virtualization benchmark being defined by the SPEC committee

- Our focus is vCon
vConsolidate Benchmark Configuration

- Various profiles for vCon.
- We chose profile 3

System config:
- Intel dual socket core2-duo machine
- Core2-duo processors at 3GHz/4MB second level cache
- 16GB system memory
- Intel VT technology

Tools:
- Xentop/sar
- Virtual Emon developed by Intel
- Xen code instrumentation
Results and Analysis: Performance Impact

- Workloads are run alone within a single VM for the dedicated measurements.
  - SPECjbb loses 37% in consolidation
  - Sysbench loses 58%
  - Webbench loses 20%
  - and Mail loses 32%
- Degradation likely to resources like core/cache/memory/IO/network contention and due to virtualization overheads
- Cpu utilization reduction is due to core contention
Results and Analysis: Architectural characterization

• Understand where the performance loss is coming from
  
  – jbb CPI (cycles per instruction) increases 37%
  
  – Most of CPI increase is due to L2 MPI (misses per instruction) increase
    – Due to cache pollution when running with other workloads
    – Cache interference
  
  – Similar behavior observed with other workloads too.
Results and Analysis: Cache Scaling

• Useful for understanding the benefit of larger caches to the workloads.
  • Jbb and Sysbench do well with larger caches.
• Helpful for platform architects in machine decision about cache sizes for future platforms

![Performance Comparison of workloads in a Dedicated vs. Consolidated environment](image)

<table>
<thead>
<tr>
<th></th>
<th>1MB</th>
<th>2MB</th>
<th>4MB</th>
</tr>
</thead>
<tbody>
<tr>
<td>JBB in vCon</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Jbb Score</td>
<td>1</td>
<td>1.31</td>
<td>1.78</td>
</tr>
<tr>
<td>Jbb CPI</td>
<td>1.77</td>
<td>0.77</td>
<td>0.57</td>
</tr>
<tr>
<td>Jbb L2 MPI</td>
<td>1.75</td>
<td>0.75</td>
<td>0.49</td>
</tr>
</tbody>
</table>

Cache Scaling for SPECjbb (in vCon)

<table>
<thead>
<tr>
<th></th>
<th>1MB-S</th>
<th>2MB-S</th>
<th>4MB-S</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sys in vCon</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Sys Score</td>
<td>1.41</td>
<td>1.76</td>
<td>1.60</td>
</tr>
<tr>
<td>Sys CPI</td>
<td>0.83</td>
<td>0.76</td>
<td>0.57</td>
</tr>
<tr>
<td>Sys L2 MPI</td>
<td>0.70</td>
<td>0.57</td>
<td></td>
</tr>
</tbody>
</table>

Cache Scaling for Sysbench (in vCon)

<table>
<thead>
<tr>
<th></th>
<th>1MB-S</th>
<th>2MB-S</th>
<th>4MB-S</th>
</tr>
</thead>
<tbody>
<tr>
<td>Web in vCon</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Web Score</td>
<td>1.06</td>
<td>1.18</td>
<td></td>
</tr>
<tr>
<td>Web CPI</td>
<td>0.92</td>
<td>0.88</td>
<td></td>
</tr>
<tr>
<td>Web L2 MPI</td>
<td>0.84</td>
<td>0.69</td>
<td></td>
</tr>
</tbody>
</table>

Cache Scaling for Webbench (in vCon)

<table>
<thead>
<tr>
<th></th>
<th>1MB-S</th>
<th>2MB-S</th>
<th>4MB-S</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mail in vCon</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Mail Score</td>
<td>1.15</td>
<td>1.09</td>
<td>1.09</td>
</tr>
<tr>
<td>Mail CPI</td>
<td>1.09</td>
<td>0.71</td>
<td></td>
</tr>
<tr>
<td>Mail L2 MPI</td>
<td>0.67</td>
<td>1.05</td>
<td></td>
</tr>
</tbody>
</table>

Cache Scaling for Mail (in vCon)
Results and Analysis: vCon Execution Profile – Life of a VM

• Understand how a VM behaves over time
• Instrumented the scheduler to give us data like on which pcpu the VM is running, how long, where it did migrate to, when did it come back and who ran while it had migrated
  – Help understand cache interference
  – Help understand the behavior of the scheduler

• We measured cpu utilization with xentop and using our instrumentation
• Data pretty close validating our methodology

<table>
<thead>
<tr>
<th>CPU%</th>
<th>Measured with Xentop</th>
<th>Computed from Scheduler Profile</th>
</tr>
</thead>
<tbody>
<tr>
<td>dom0</td>
<td>30%</td>
<td>36%</td>
</tr>
<tr>
<td>JBB</td>
<td>122%</td>
<td>120%</td>
</tr>
<tr>
<td>Sys</td>
<td>116%</td>
<td>118%</td>
</tr>
<tr>
<td>Web</td>
<td>114%</td>
<td>112%</td>
</tr>
<tr>
<td>Mail</td>
<td>6%</td>
<td>8%</td>
</tr>
</tbody>
</table>
Results and Analysis: vCon Execution Profile – Life of a VM

- A VM runs on all pcpus, no particular affinitization
  - Shows good dynamic load balancing by the scheduler.

- A VM comes back to the same pcpu most of the time
  - Helps in reducing cache misses, if the cache was not polluted
  - What is missing is: what VMs ran during the interim that the VM had migrated – extent of cache pollution/ quantification of cache misses

<table>
<thead>
<tr>
<th>VM</th>
<th>Across all cpus</th>
<th>pCPU0</th>
<th>pCPU1</th>
<th>pCPU2</th>
<th>pCPU3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dom0</td>
<td>100%</td>
<td>19%</td>
<td>33%</td>
<td>27%</td>
<td>8%</td>
</tr>
<tr>
<td>Jbb</td>
<td>100%</td>
<td>32%</td>
<td>28%</td>
<td>20%</td>
<td>25%</td>
</tr>
<tr>
<td>Sys</td>
<td>100%</td>
<td>26%</td>
<td>25%</td>
<td>28%</td>
<td>24%</td>
</tr>
<tr>
<td>Web</td>
<td>100%</td>
<td>18%</td>
<td>20%</td>
<td>27%</td>
<td>40%</td>
</tr>
<tr>
<td>Mail</td>
<td>100%</td>
<td>37%</td>
<td>23%</td>
<td>18%</td>
<td>2%</td>
</tr>
</tbody>
</table>

Time profile of a vcpu across physical cpus

<table>
<thead>
<tr>
<th>VM</th>
<th>% time came back to same cpu</th>
<th>% Time went to another cpu</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dom0</td>
<td>95%</td>
<td>5%</td>
</tr>
<tr>
<td>JBB</td>
<td>87%</td>
<td>13%</td>
</tr>
<tr>
<td>SYS</td>
<td>92%</td>
<td>8%</td>
</tr>
<tr>
<td>WEB</td>
<td>92%</td>
<td>8%</td>
</tr>
<tr>
<td>MAIL</td>
<td>97%</td>
<td>3%</td>
</tr>
</tbody>
</table>
Results and Analysis: vCon Execution Profile – Cache Interference

• Cache interference impacts the performance of the workload
• Find out which VM/vcpu shares the second level cache with another VM/vcpu and for how much time
• Of the 30.3% spent in jbb, 10.5% is spent with sysbench, 10.2% with webbench, 7.5% with jbb (other vcpu) 2% with Mail
• Knowing the L2 MPI and CPI impact of a VM with another VM we can determine the cache interference

![Graph showing % Time a VM ran with another VM](image-url)
Results and Analysis: vCon Execution Profile – Cache Interference

- Affintize the vcpus to different cores
- Get cache MPI with each of the workloads

- Jbb loses 16% running with JBB, 14 with Sysbench, 11 with Webbench and 3% with Mail

Impact to SPECjbb L2 MPI due to running with other workloads

Impact to SPECjbb CPI due to running with other workloads
Form characterization to Modeling

- How do we build a performance projection model
What kind of hooks would we like?

• How can we get the execution profile of a VM without instrumentation?

• Have some logging in the VMM that gives:
  – Where (which pcpu) did the VM start running, what other VMs were running on all other pcpus.
  – Where (which vcpu) did the VM/vcpu migrate to?
  – When did the vcpu come back to the same pcpu?
  – How long did it run on the pcpu before migration?
  – What was the longest duration a VM ran on any pcpu/with another VM

• While the VM was running, what events (context switches, interrupts (external and ipis) page faults, tlb flushes etc were generated, and what was the time taken by the VMM to service these events.
Summary

• Need for performance characterization of a consolidation benchmark

• Introduction of the vConsolidate benchmark developed by Intel

• Dedicated vs. Consolidated performance
  – Sysbench and SPECjbb lose the most performance
  – Degradation due to contention of resources

• Architectural characterization for cache interference
  – Scheduling studies to understand the life of a VM/vcpu
  – Affinitization studies to understand cache interference

• Overview of building a performance projection model

• Xen hooks needed
Thank you

Questions?
How best to provision the Applications so as to meet performance and SLA criteria?

What platform/cpu features are needed in the future to support server consolidation?

How to improve and optimize scheduling algorithms for resource management and consolidation?

IT Infrastructure and Management

Future Platform/cpu Architectures

VMM Optimizations

Server Consolidation Characterization and Modeling